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傅小虎

# Bruno Formica

## Microtechnology Engineer

### Personal Data

Place of birth : Geneva, Switzerland  
Nationalities : Swiss & French

Date of birth : 8 July 1983  
DNA : Enthusiastic – Productive – Creative

### Assets

English – French – German – Chinese

Eager for work – Enjoying multicultural teams – At ease with various assignments

### Objectives

Enthusiast Junior Engineer willing to work in an international environment.  
Seeking an opportunity to apply and improve my skills on innovative projects.  
My goals are to be able to provide efficient solutions that establish – monitor – evaluate industrial projects with user friendly products/services as a successful output.  
Keen interest in management techniques, methodologies and clean technologies.

### Work experience

Since Winter 2010

**Administrative Management Trainee, Information Technologies Centre**  
Managing Pre/Post Production Resource – Improving Production Processes (LEAN,5S)  
Emphasizing Best Practices with available IT Solutions  
Financial & Economic Data Printing and Mailing, State of Geneva

2008 - 2010

**Business Development Assistant, SHARE Startup Company**  
Assist definition, prospection & analysis  
business plan - business models - market segments - communication scheme  
Mobility On Demand, Transportation Resource Access Management & Services

Spring 2010

**Teacher Assistant, State of Vaud**  
Lecturing for grade 6 students - Supervising in class tests  
Public Middle School of St-Rock, Lausanne

Autumn 2009

**Project Engineer Intern, BioMobile Association**  
Power Electronics State of the Art, Selecting & Characterizing Components  
Adapting weight, size & consumption imperatives with financial & mandatory constraints  
Solutions for an electronic ignition device commanding a non ordinary power train  
Very Low Consumption Vehicle project

Autumn 2005

**VLSI Analog Designer, CERN Microelectronic group**  
A/D Converters State of the Art, Bandwidth, Resolution Vs Consumption characterization  
Assess basic Analog structures (Buffers, Inverters, Amplifiers) in CMOS 250 nm technology  
Optimizing converting speed performances and power consumption of a given structure  
Low Resolution & Low Power CMOS Folding A/D Converter project

Summer 2004

**Electrician, Atel Bornet SA**  
Managing the installation of a Smoke Detection System – Assist other electrical installations  
Vögele garment store building, Rue de la confédération, Geneva

Summer 2000

**Trainee, Firstec SA**  
Inventorying Components – Operating Production Appliances – Testing Printed Circuit Boards  
Electronics Manufacture, Plan-les-Ouates, Geneva

## Additional studies

Autumn 2010	<p><b>Metrology, Six Sigma &amp; Lean Management Event</b> Introducing productivity techniques, Swiss Engineering Congress School of Business Administration, Neuchâtel, Switzerland</p>
2008 – 2009	<p><b>Chinese Language &amp; Culture Program</b> Summer program + 2 regular semesters, Full time intensive courses International College of Zhejiang University, Hangzhou, China</p>
2006 – 2007	<p><b>Intensive Applied Mathematics, Linear Algebra and Physics training courses</b> Microtechnology Section of the EPFL Swiss Polytechnic Institute Lausanne, Switzerland</p>
Spring 2006	<p><b>Nano CMOS / more Moore Event</b> 2 days state of the art tutorials IMEC Academy, Leuven, Belgium</p>
2004 – 2005	<p><b>Applied Acoustics</b> 2 semesters additional course University of Applied Sciences of Western Switzerland, Geneva</p>

## Talent Check

[www.jobs.ch/talentcheck](http://www.jobs.ch/talentcheck)



## Education

January 2006	<p><b>Bachelor of Engineering with distinction, Degree course in Microelectronics</b> VLSI Analog Design – CERN Microelectronic group Foundation Hans Wilsdorf Award ( ROLEX )</p>
2001 – 2005	<p><b>Bachelor of Engineering in Microtechnology with an emphasis on Microsystems</b> University of Applied Sciences of Western Switzerland, Geneva ( HES-SO ) 200 ECTS credits</p>
1997 – 2001	<p><b>Technical Degree in Mechanics &amp; Electricity</b> Technical High School, Engineering School of Geneva ( EET, EIG )</p>

## Languages

<b>French</b>	Mother tongue
<b>English</b>	Good skills, Upper Intermediate ( TOEIC score: 770/990 )
<b>German</b>	Elementary basis ( Goethe Institute Deutsch, elementary diploma n°1 )
<b>Chinese</b>	Elementary basis ( Zhejiang University Certificate )

## Extracurricular

- Since 2006 **Member of the “Swiss Society for Sensor Technology” association ( Sensors.ch )**  
Visits and events providing sensor technologies know-how, dedicated industries networking
- Since 2003 **Member of the “Swiss Engineering” association , Geneva’s group**  
Visits and events representing the biggest social network of engineers and architects in Switzerland
- Since 2002 **Member of the “Swiss-China bridge” association in Geneva ( Pont CH-Chine )**  
Participation in visits and events across China, emphasizing on social and cultural exchanges
- 1993 – 2003 **Music study of Clarinet and Saxophone with some performances**
- 1989 – 2002 **Member in many local Sport Clubs**  
Football, Track & Field, Judo, Base-ball, In-line Skating

## References

- HES-SO** Prof. J-L Bolli, jean-luc.bolli@hesge.ch  
Prof. J-M Allenbach, jean-marc.allenbach@hesge.ch
- CERN** Francis Anghinolfi, francis.anghinolfi@cern.ch
- SHARE** Eric Pajot, contact@pajot.ch
- SE-GE** Yvette Ramos, Ingénieure, CEO Firstec SA, Présidente SE-GE, y.ramos@firstec.ch
- CHINA** 罗时武, Jiangxi University of finance and economics, Vice-dean, luoshiwu@163.com